

# Design and fabrication of high-index-contrast self-assembled texture for light extraction enhancement in LEDs

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**Abstract:** We developed a high-index-contrast photonic structure for improving the light extraction efficiency of light-emitting diodes (LEDs) by a self-assembly approach. In this approach, a two-dimensional grating can be non-lithographically integrated on the top of virtually any types of LEDs with controlled structural parameters and material indices. As a proof of concept, our designed photonic structure was implemented on a GaAs double heterojunction LED. Using numerical electromagnetic simulations, we explored the effects of the structural parameters (the grating period, layer thickness and material indices) on the device performances, followed by fabrication through a self-assembled porous alumina as a template. Device simulation and experimental results indicate that an optimized high-index-contrast (a-Si / air) grating obtains a much larger efficiency increase than using a low-index SiO<sub>2</sub> grating. In addition, the devices maintain a Lambertian radiation pattern with the self-assembled grating. This technique provides an effective and low-cost method for improving LED efficiency.

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## 1. Introduction

Light-emitting diodes (LEDs) have become an emerging and promising candidate for light sources. Unlike conventional incandescent or fluorescent light sources, LEDs directly transfer electricity into light. Therefore, it has many advantages such as high energy conversion efficiency, long lifetime and small footprint [1]. Typical LED devices are made of inorganic (III-V) or organic semiconductor junctions. The internal quantum efficiency  $\eta_i$  of the light emission process, which is the portion of electron-hole pairs converted into photons, can be increased by improving material quality, and the state-of-the-art  $\eta_i$  has reached 90%, already approaching the theoretical limit [2]. As a consequence, currently the major LED efficiency limiting factor becomes poor light extraction, i.e. photons trapped inside the device due to total internal reflection at semiconductor and air interface. For a planar LED, the light extraction efficiency  $\eta_e$  can be estimated to be  $1/4n^2$  [3]. For most semiconductors, their refractive indices  $n$  are high ( $n > 2$ ). Therefore,  $\eta_e$  is a severe limitation for overall device performance.

To resolve the issue of limited extraction efficiency  $\eta_e$  in semiconductor LEDs, many photonic designs have been proposed. For example, organic materials are used as encapsulants to cover the top surface and reduce the total internal reflection [4]. Another example is to roughen/texture the device surface and induce stronger scattering effect [5,6]. However, these methods have limitations. Organic encapsulants suffer from degradation under photon radiation (especially for blue and green light), which largely limits their use for long lifetime and high brightness LEDs. Texturing semiconductor surface (by plasma etching, for example) generates defects that work as recombination centers that exacerbate non-radiative recombination and decrease the internal quantum efficiency  $\eta_i$  [7]. Furthermore, random scattering surfaces generated using a conventional texturing process lack the ability to purposely optimize and control the structural parameters of the textured surface.

Photonic crystal (PhC) has a structure with one, two, or three dimensional (1D, 2D and 3D) periodic patterns consisting of two materials with different refractive indices. In such periodic structures, light propagation can be controlled in various ways [8,9]. To increase LED extraction efficiency, a two-dimensional photonic crystal structure can be embedded in the device [10] or integrated on top surface [11,12]. Due to the periodicity, the guided waves can leak to free space as Bloch modes. Therefore, light can escape from the high index semiconductors even if the incident angle is larger than the critical angle. However, to fabricate these photonic crystals with submicron feature size, high-cost methods like interference lithography or electron beam lithography have to be employed, which prohibits LED cost reduction and high-volume production. Therefore, a low-cost and controllable method is highly desirable for fabrication of light extraction photonic crystal structures in LED devices.

Here, we proposed a self-assembled two-dimensional photonic structure to increase the light extraction efficiency in LEDs. The proposed structure has several advantages: (1) A potentially low-cost, self-assembled method is introduced for fabrication, and the structural parameters can be controlled by experimental conditions; (2) Unlike plasma or wet chemical etching, the method is non-specific and can be implemented on top of any semiconductors; (3) Instead of etching through the active device, the structure is directly deposited on the surface, so it is non-damaging and does not cause any degradation of internal quantum efficiency  $\eta_i$ ; (4) According to the targeted emission wavelength, various materials can be deposited to make the structure for achieving optimal light extraction; (5) As we will discuss below, a high-index-contrast texture is used, which provides much more efficient light extraction than low-index-contrast structures like  $\text{SiO}_2$  / air or  $\text{Al}_2\text{O}_3$  / air gratings.

We organize the paper as follows: Section 2 described a p-AlGaAs / i-GaAs / n-AlGaAs double heterojunction LED used in our experiments. Section 3 numerically explored the effects of the structural parameters as well as material index contrast for the proposed structure. The self-assembled technique for grating fabrication is shown in Section 4. In Section 5, we analyzed the performances of the light extraction structures. By using the optimized high-index-contrast grating, the light emission intensity is improved by 27%. Here we want to point out that even though similar 2D grating designs have been applied to increase light extraction in LEDs, the gratings used in previous reports are low-index gratings. As we show in Sections 3 and 5, such low-index gratings have inherently limited light extraction capabilities. In contrast, our self-assembly processing is highly flexible in grating material selection and thus enables significant extraction enhancement using high-index gratings.

## 2. Device structure

The proposed photonic structure is integrated onto a conventional GaAs based LED, as shown in Fig. 1(a). A double heterojunction was grown on an n-type GaAs substrate by Metal-Organic Chemical Vapor Deposition (MOCVD). The detailed structure (from bottom to top) is 1  $\mu\text{m}$  n- $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$  (Si doping,  $n = 4 \times 10^{18} \text{ cm}^{-3}$ ), 0.12  $\mu\text{m}$  intrinsic GaAs and 1  $\mu\text{m}$  p- $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$  (Zn doping,  $p = 4 \times 10^{18} \text{ cm}^{-3}$ ). Subsequently, a thin p-GaAs layer (200nm, Zn doping,  $p = 1 \times 10^{19} \text{ cm}^{-3}$ ) is grown on top of the p-i-n junction as a protecting layer to prevent the  $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$  layer from oxidation and enable a better ohmic contact. Ideal ohmic contacts on the n-GaAs and p-GaAs are formed by 20 nm Ni / 30 nm Ge / 200 nm Au and 20 nm Ni / 30 nm Zn / 200 nm Au, followed by rapid thermal annealing (at 400 °C for 40 seconds). The above procedures create the reference LED device without light extraction structures.

The top view of designed light extraction configuration is plotted in Fig. 1(b), made by a hexagonal pattern as a grating layer. Due to the diffraction effect of the grating, light propagating at oblique angles (outside the emission cone) can be partly coupled out from the high index III-V semiconductor layer. Therefore, the light extraction efficiency will be significantly improved. Since the light out-coupling critically depends on the light extraction

structure, the grating should be carefully designed to maximize the efficiency at the device emission wavelength.

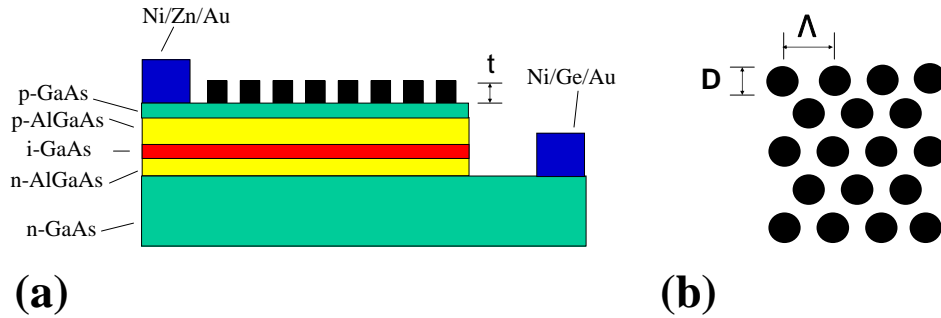


Fig. 1. (a) Schematic layout of a test LED device with photonic crystal structures integrated on top surface for light extraction. The active device is a double heterojunction based on a GaAs substrate. (b) Top view of the photonic crystal layer, which has a hexagonal lattice, with period  $A$ , thickness  $t$  and rod diameter  $D$ .

### 3. Numerical simulation

#### 3.1 Optimization of grating parameters

A three-dimensional model based on rigorous coupled wave analysis (RCWA) was utilized to optimize the performance of the grating design. The basic theory can be found in the reference [13]. The simulated device structure used in this model is described in the previous section, excluding the metallic contact pads. The optical properties (refractive index  $n$  and extinction coefficient  $k$ ) for all the materials are quoted from E. Palik [14]. The simulation is performed at the central emission wavelength, which is 870 nm corresponding to the bandgap of GaAs ( $E_g = 1.43$  eV). A monochromatic dipole is placed in the middle of the intrinsic GaAs layer and the emitting field intensity on top of the grating layer is integrated over all angles to calculate the extraction efficiency. Unlike nitride based semiconductor, GaAs has a zinc blende structure, which leads to an isotropic spontaneous emission. Therefore, the overall extraction efficiency is averaged over different dipole orientations and dipole positions with respect to the grating unit cell.

Here we show that the structure and optical parameters of the gratings (refractive index, grating period  $A$ , cylinder diameter  $D$  and thickness  $t$  in Fig. 1(b)) can significantly impact the light extraction efficiency. Here we assume the grating filling area ratio is 1:1, i.e.,  $D = 0.74 A$ , and study the impact of varying  $A$  and  $t$ . To quantify the effect of grating index contrast, we simulated two types of gratings, amorphous silicon (a-Si) in air matrix and silicon dioxide ( $\text{SiO}_2$ ) in air matrix. Figure 2(a) and 2(b) plot the relative LED efficiency enhancement as functions of  $A$  and  $t$  for both types of gratings. Figure 2(a) reveals that when we choose appropriate values ( $A$  and  $t$ ) for the a-Si / air grating, strong light extraction effect appears and the LED performance is enhanced. According to the simulation, the optimized design corresponds to  $A = 500$  nm and  $t = 200$  nm, where an enhancement factor of up to 70% can be achieved. Although the  $\text{SiO}_2$  / air grating has a similar optimized region ( $A = 600$  nm and  $t = 250$  nm), the optimal enhancement factor is much lower (about 18%). This suggests that the a-Si / air grating is very effective in enhancing light extraction, because of the high index contrast (4.0 versus 1.0) of this system as well as the transparency of a-Si in the near-infrared wavelength range. In comparison, the optimal enhancement is far less significant in the  $\text{SiO}_2$  / air grating design (Fig. 2(b)) due to the much lower index contrast (1.4 versus 1.0).

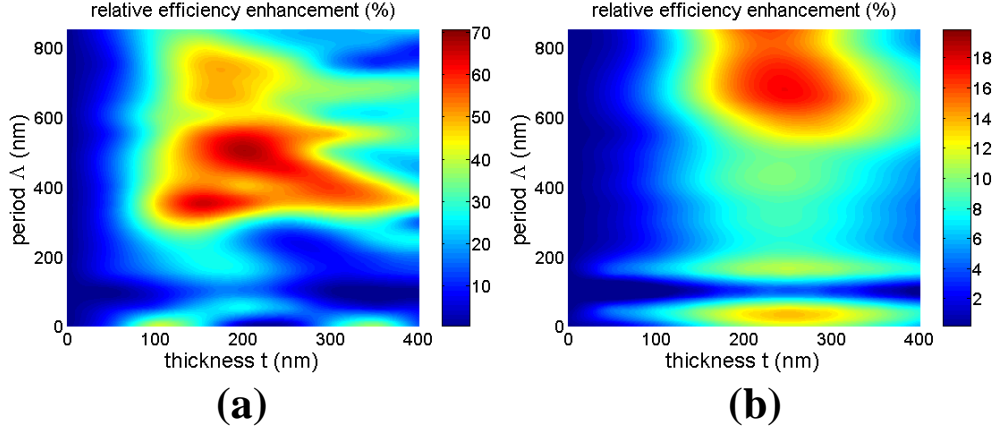


Fig. 2. Plot of the relative efficiency increase after introducing the grating layer for light extraction, as a function of grating period  $\Delta$  and thickness  $t$ . The performance is compared with the planar device without the grating. Grating structure: (a) a-Si hexagonal pattern in air matrix; (b) SiO<sub>2</sub> hexagonal pattern in air matrix.

### 3.2 Calculation of far field emission profiles

To further understand the effects of high-index-contrast grating, we analyzed the far field emission pattern for the LED structure with the optimal grating parameters (a-Si / air grating with  $\Delta = 500$  nm and  $t = 200$  nm). Figure 3 compares the contour plots for the LEDs with and without grating in the reciprocal space. In Fig. 3(a) and 3(b),  $k_x$  and  $k_y$  are parallel to the device plane, and each point  $(k_x, k_y)$  corresponds to a specific emission angle  $\theta$  in the far field,

$$\sin \theta = \frac{\sqrt{k_x^2 + k_y^2}}{2\pi / \lambda_0} = \frac{\sqrt{k_x^2 + k_y^2}}{k_0} \quad (1)$$

The averaged angular dependences are plotted in Fig. 3(c). As expected, the planar device reveals a Lambertian emission pattern with a maximum in the normal direction, since GaAs has an isotropic spontaneous emission. For the LED with grating, the non-Lambertian behavior is caused by the outcoupling of the guided modes due to diffraction, which also explains the improvement of extraction efficiency. Because the results are averaged among dipoles emitting at different directions and combining both TE and TM modes, no significantly sharp peaks can be observed as mentioned in some references [11,15].

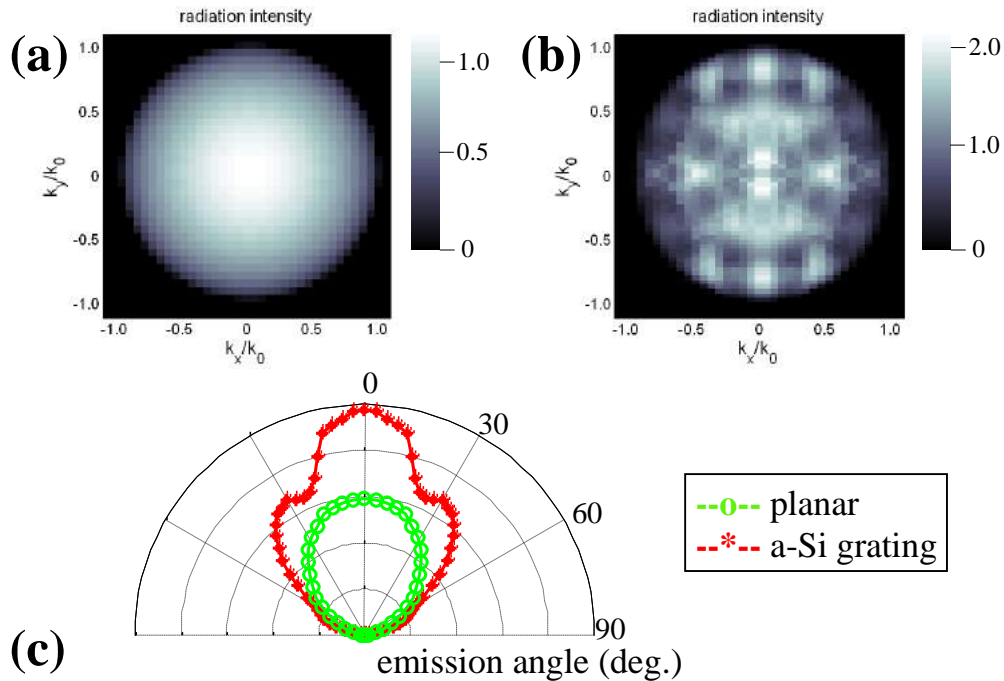


Fig. 3. Simulated far-field emission pattern at a wavelength of 870 nm. (a) Planar LED without grating on top, which shows a Lambertian pattern. (b) LED with a periodic a-Si / air grating of optimized parameters ( $A = 500$  nm and  $t = 200$  nm), which shows a non-Lambertian pattern. (c) Averaged angular dependence of light emission for devices without (green) and with (red) a-Si grating. The normalized intensity has an arbitrary unit.

#### 4. Fabrication and characterization of the self-assembled light extraction structures

The above simulation results predict and optimize the performance of devices with a perfectly periodic texture. To fabricate the optimized sub-micron light extraction structure on top of the LED devices, one needs to resort to lithographic techniques. If the light extraction structure only has short range order, the device performances will deviate from the simulations because of the average effect due to imperfection. However, the influences of different index contrast can still be observable in those short-range-order structures.

To demonstrate the effect of refractive index contrast on extraction efficiency, we use low-cost, self-assembled anodic aluminum oxide (AAO) templates as an alternative to high resolution lithography. AAO has a highly porous structure, of which the feature size (inter-pore distance, pore size, etc.) can be modulated by the anodization condition, and it has been used directly as light extraction structures in some previous work [16,17]. However, as we demonstrated in the above simulations, the low index contrast between air and alumina (1.0 versus 1.7) will significantly limit the extraction enhancement. Also, the electrolyte used in the anodization process can severely degrade the active device by introducing surface defects. To overcome these challenges of directly using AAO for light extraction, we used AAO as a template. Details of the process can be referenced in [18]. By a two-step anodization at a constant DC voltage of 200 V in a 1.0 M citric acid solution, self-organized AAO with a uniform pore distribution can be fabricated and used as a deposition mask. Figure 4(a) shows a SEM image of a fabricated AAO membrane with a period of about 500 nm and thickness of nearly 1  $\mu\text{m}$ . Through this thin porous membrane, various materials including semiconductors and metals can be deposited via simple thermal or electron beam evaporation. As we designed in the previous section, 200 nm a-Si and  $\text{SiO}_2$  are deposited on top of the GaAs LEDs respectively, forming the optimal designed light extraction structure. Shown in the AFM

images (Fig. 4(b) and 4(c)), the deposited a-Si and SiO<sub>2</sub> pattern have very similar morphology, directly replicating the near hexagonal AAO pore arrays.

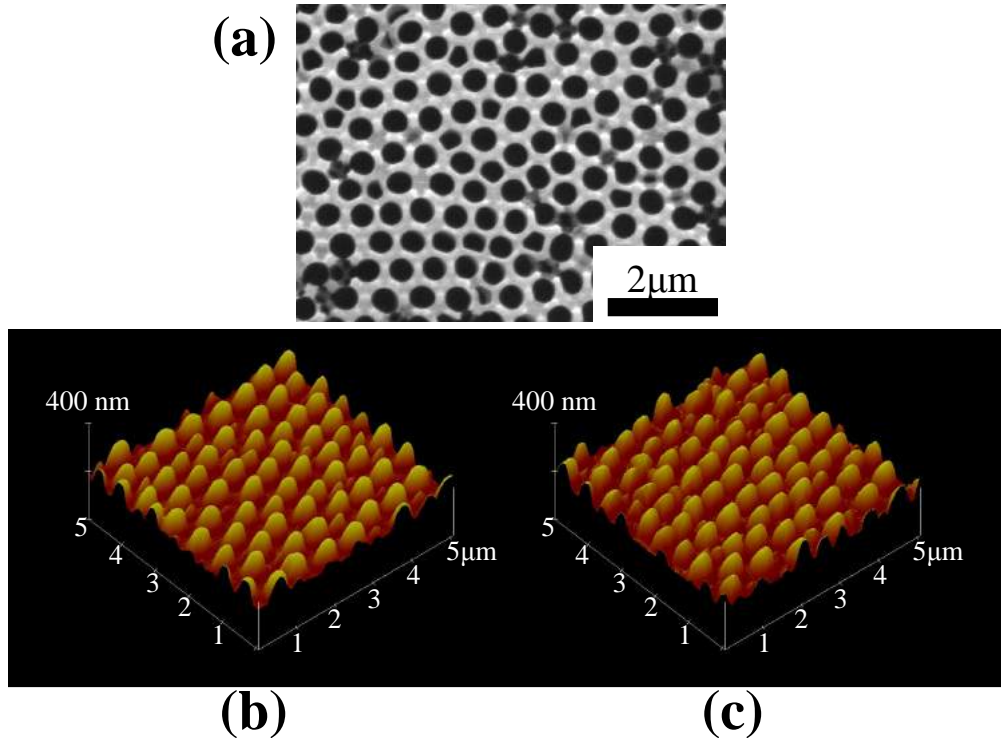


Fig. 4. (a) SEM image of the anodized porous alumina membrane utilized as a deposition mask; (b) AFM image of the deposited a-Si pattern; (c) AFM image of the deposited SiO<sub>2</sub> pattern.

## 5. Experimental results and discussion

The electroluminescence (EL) performances of LEDs with different light extraction structures were measured using an optical multimeter (Ando AQ2140). A multimode fiber (100 μm diameter, NA = 0.28) was placed above the devices to collect the emitted photons from the surface. The light intensity-current (*L-I*) characteristics are shown in Fig. 5(a). The slight non-linear behavior is due to some feedback mechanisms, causing a small portion of stimulated emission [19]. It can be clearly seen that the emission intensity is significantly higher if the designed grating is used as a light extraction structure. Compared to the planar device without any light extraction structure, the device with a-Si grating shows a relative efficiency improvement of 27%, while the SiO<sub>2</sub> grating only achieves 7.3% relative increase. These results are further confirmed by the emission spectra measured under a current of 20 mA by an optical spectrum analyzer (Ando AQ6315A). As illustrated in Fig. 5(b), all the spectra peak at 870 nm, with a full width at half-maximum (FWHM) of 26 nm. The *L-I* curves and emission spectra clearly reveal that the grating with higher index contrast entails higher performance for light extraction, which agrees well with our numerical predictions. Other approaches show a 130% increase in GaN devices by surface roughening [6] and 39% increase in AlGaInP directly using the low-index AAO [17]. The differences in materials systems (GaAs vs. GaN or AlGaInP) and device configurations (bulk vs. thin film) lead to lower efficiency improvements in our case. However, our approach can be implemented in those different systems and will yield better performances which scale with index-contrast and structural parameters according to our model.



Figure 5(c) shows the angular dependence of emission for the devices with and without a-Si grating. In the measurement, the devices are mounted on a goniometer which can rotate 90 degrees. Unexpectedly, both the devices show a Lambertian emission pattern, which does not fully agree with the simulation predictions shown in Fig. 3(c). This is mainly due to the imperfection of the fabricated grating. As seen in the SEM image of AAO membrane (Fig. 4(a)), the pore distribution only exhibits short-range order. In addition, the AFM images of the deposited a-Si and SiO<sub>2</sub> indicate we obtained a cone-shaped array. Therefore, the self-assembled approach causes a deviation from the ideal hexagonal array of cylinders, thus inducing random scattering and forming the near-Lambertian emission pattern [20].

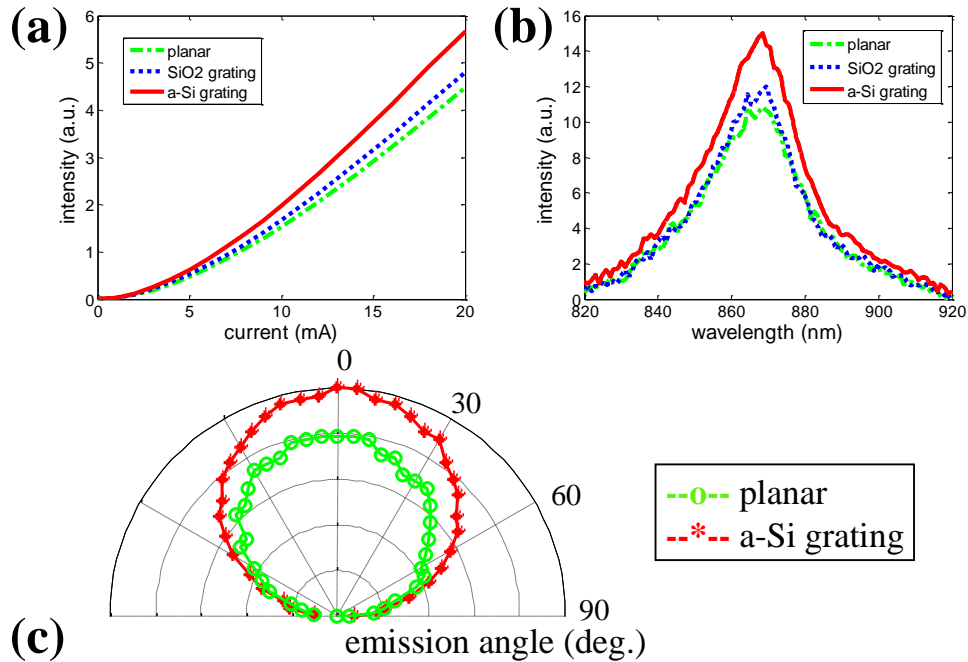


Fig. 5. Performances for LED devices with various top structures: the planar LED without grating (green), the LED with SiO<sub>2</sub> grating (blue) and the LED with a-Si grating (red). (a) Light intensity-current (*L-I*) curves; (b) Emission spectra measured at current of 20 mA. It can be observed that the LED with a-Si grating shows the highest emission intensity, thus obtaining the highest efficiency. (c) Angular resolved measurement of light emission for the planar LED and the one with a-Si grating. The normalized intensity has an arbitrary unit.

Although the above results are obtained from GaAs based near-IR LEDs, the proposed design can also be applied for light extraction in LEDs emitting at other spectral ranges. For visible LEDs based on nitride or organic semiconductors, the simulation and fabrication methods we utilized here are still applicable, while the structural parameters should be altered to accommodate the target wavelength. The AAO template has controllable feature sizes, which can be manipulated by varying experimental conditions like applied voltage and anodization time [21]. It should be noted that a-Si becomes strongly absorptive at visible wavelengths ( $< 700$  nm). Therefore, other materials should be exploited to make the self-assembled pattern. Possible candidates can be SiC and ZnS, both of which have high refractive indices as well as low absorption coefficients in the visible range [14].

## 6. Conclusion

In this paper, a self-assembled light extraction structure was integrated for LED efficiency enhancement. Based on numerical simulations, we systematically investigated the impact of grating parameters and materials selection on the light extraction. We showed that high-index



gratings are far more effective in light extraction improvement compared to their low-index counterpart. The optimized structures were integrated on GaAs based LEDs by using self-assembled AAO membranes as a template, which acts as an effective and low-cost alternative to lithographic fabrications. The LED device with the optimized high-index-contrast a-Si grating yielded a 27% improvement in emission intensity. The effects of different index contrasts were verified by comparing the performances of gratings made from a-Si and SiO<sub>2</sub>. Unlike the simulation results, the device with fabricated a-Si grating showed a Lambertian emission pattern, which is due to the non-ideality of the periodicity. By proper structure design and materials selection, these approaches could also provide a guideline for efficiency enhancement in nitride and organic based LEDs.

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